

000746582
365 201
Class Subclass
ISSUE CLASSIFICATION

5511029

UTILITY
SERIAL
NUMBER 08/246582

PATENT DATE
APR 23 1996

PATENT
NUMBER



SERIAL NUMBER 08/246,582	FILING DATE 05/19/94	CLASS 365	SUBCLASS 201	GROUP ART. UNIT 2511	EXAMINER ZARABIAN
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SEIJI SAWADA, HYOGO, JAPAN; YASUHIRO KONISHI, HYOGO, JAPAN.

CONTINUING DATA***
VERIFIED *None*

FOREIGN/PCT APPLICATIONS***
VERIFIED JAPAN 5-122439 05/25/93

Foreign priority claimed 35 USC 119 conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	AS FILED	STATE OR COUNTRY JPX	SHEETS DRWGS. 29	TOTAL CLAIMS 36	INDEP. CLAIMS 11	FILING FEE RECEIVED \$1,654.00	ATTORNEY'S DOCKET NO. 3941597
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Verified and Acknowledged
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ISSUE FEE IN FILE

TEST CIRCUIT IN CLOCK SYNCHRONOUS SEMICONDUCTOR MEMORY DEVICE

U.S. DEPT. of COMM.-Pat. & TM Office-PTO-436L (rev. 10-78)

PARTS OF APPLICATION FILED SEPARATELY		10-26-95 Applications Examiner	
NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
10-26-95		Total Claims 33	Print Claim 9
ISSUE FEE		DRAWING	
Amount Due 1250.00	Date Paid 1-22-96	Sheets Drwg. 29	Figs. Drwg. 39
Label Area		Print Fig. 17	ISSUE BATCH NUMBER Q42
DAVID C. NELMS SUPERVISORY PATENT EXAMINER GROUP 2500 Primary Examiner			
PREPARED FOR ISSUE			
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(FACE)

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